Review and DE-1 Demo  
(Class 6.2 – 2/21/2013)

CSE 2441 – Introduction to Digital Logic 
Spring 2013
Instructor – Bill Carroll, Professor of CSE
Today’s Topics

• Review HW #4 solutions
• Exam 1 coverage
• DE-1 programming demo
• Questions and answers
HW #4 Solutions

See posted solutions
Examination 1 Coverage

- Chs 0-3 -- *Nelson, Nagle, Carroll, and Irwin*
- Topics
  - 2’s complement number systems
  - Alphanumeric codes – BCD, ASCII
  - Basic logic gates
  - Logic functions
    - Truth tables
    - Algebraic expressions
    - Canonical forms -- SOP, POS
    - Minterm/maxterm lists
  - Minimum forms – MSOP, MPOS
  - Minimization procedures
    - Boolean algebra
    - Karnaugh maps
    - Quine-McCluskey
- Combinational circuits
  - Analysis – algebraic, truth table, timing diagrams
  - Design – 2-level, 3-level, multi-level (factoring)
Demonstration of DE-1

See posted Lab 5