The University of Texas at Arlington

Class Introduction

CSE 2441 – Introduction to Digital Logic
Dr. Gergely Záruba

Instructor

- Dr. Gergely Záruba – Office: ERB523
- Office Hours: TT 11:00am – 12:00pm other times by appointment only
- Email: zaruba@uta.edu
- Web Page: http://crystal.uta.edu/~zaruba
- Lab Instructor: Sudhamsh Reddy
  Office hours: Lab times in ERB127. sudhamsh@uta.edu
- Class web site:
  http://crystal.uta.edu/~zaruba/CSE2340/
Class Mailing List

- Class mailing list: CSE2441-zar@listserv.uta.edu
- Students are strongly encouraged to sign up for the mailing list
- Please either request membership via the listserv provided web interface OR send me an email within the first week of classes with the subject: CSE2340 and the body containing your email address (only).

Pre-reqs.

- You need to know the information covered in these courses.
  - CSE 1320 (Intermediate Programming)
  - CSE 2315 (Discrete Structures)
Text Books/Material


• Lab Manuals
• Reference materials, if needed, will be placed in Library on reserve and in Lab

Class Attendance Policy

• Grades are typically directly related to class attendance.
• It’s better to come to class late than miss.
# Grades - CSE2441

- **LogicWorks and homework assignments (25%)**
  - There will be no make up for assignments/homework. The maximum grade given for assignments/homework will decline by 20% of the total grade each calendar day starting razor sharp after the deadline.

- **Two Midterms: (40% total; 20% each)**
  - October 13th, 2011 and December 1st, 2011. There will be no make up!

- **Lab practicum tasks (25%)**
  - There will be no make up for assignments. The maximum grade given for assignment will decline by 20% of the total grade each calendar day. At least 75% of the required lab practicum tasks must be submitted in order for a student to pass CSE 2441. All must be submitted in order to have the possibility of making an A.

- **ABET outcome task – experimentation (Final Lab) (10%)**
  - Material covered in this project/exam will be based on the practical class experiences and required preparatory work for the lab practicum tasks.

- **A grade of zero will be recorded on any absence from an exam.**

# Papers & Lab Assignments

- Assignments must be turned in by due date or will considered late.

- 20 percent (of the maximum possible grade) per day will be deducted from all late home work starting sharp at the deadline.
Labs/Home Work

• Occasionally an ‘in class assignment’ HW may be given. Only those in attendance will receive credit for any ‘in class assignment’.

Ethics

• Policy on cheating --- students who violate University rules on scholastic dishonesty are subject to disciplinary penalties, including the possibility of failure in the course and dismissal from the University. Since dishonesty harms the individual, all students, and the integrity of the University, policies on scholastic dishonesty will be strictly enforced.
Course Topics

- This course focuses on combinational and sequential digital logic and circuits. Specific topics include:
  - Algebraic Methods for the analysis and Synthesis of Logic Circuits (Ch. 2)
  - Simplification of Switching Functions (Ch. 3)
  - Modular Combinational Logic (Ch. 4)
  - Combinational Circuit Design with Programmable Logic Devices (Ch. 5)
  - Introduction to Sequential Devices (Ch. 6)
  - Modular Sequential Logic (Ch. 7)
  - Analysis and Synthesis of Synchronous Sequential Circuits (Ch. 8)
  - typical applications
  - A lab accompanies the lecture where design concepts are investigated and implemented.

Questions